

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10044427	11/09/2001	257	327	2815	

****APPLICANTS:** Herman Thomas; Davis Harold; Spring Kyle; Cao Jianjun;

BEST AVAILABLE COPY

****CONTINUING DATA VERIFIED:**

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	IR-1641
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	
Verifier and Acknowledged Examiner's initials		
TITLE : Mosfet with reduced threshold voltage and on resistance and process for its manufacture		
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE			DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
		Primary Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH: DISK (CRF) CD-ROM
(Attached in pocket on right inside flap)